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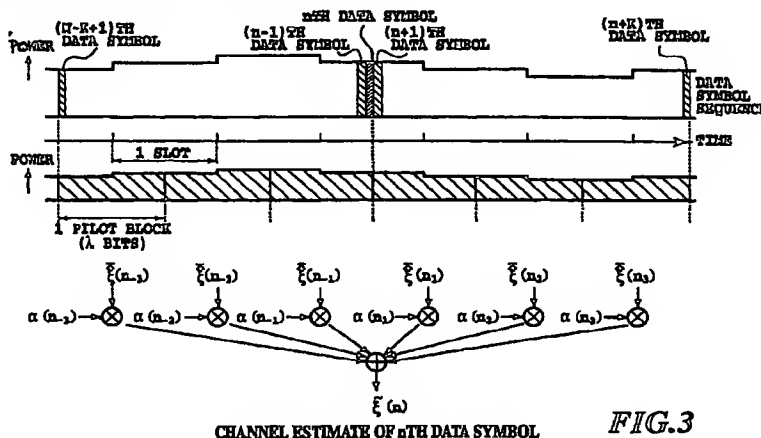
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(54) CHANNEL ESTIMATING APPARATUS, AND CDMA RECEIVER AND CDMA TRANSCEIVER
EACH HAVING THE APPARATUS

(57) There are provided a channel estimation unit for achieving highly accurate channel estimation, a CDMA receiver and a CDMA transceiver with the channel estimation unit. Channel estimates of data symbols are obtained from a pilot symbol sequence which is parallel with a data symbol sequence. First, a plurality of pilot blocks are generated from the pilot symbol sequence. The channel estimates of the data symbols are obtained by calculating a sum of appropriately

weighted averages of pilot symbols in the individual pilot blocks. This enables highly accurate channel estimation. More accurate channel estimation can be achieved by carrying out the channel estimation of the data symbols using the pilot symbols belonging to other slots rather than limiting to the pilot symbols in the slot to which the estimated data symbol belongs.



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Description

TECHNICAL FIELD

5 [0001] The present invention relates to a device for making channel estimation (propagation path estimation) of data symbols from a pilot symbol sequence parallel to a data symbol sequence, and a CDMA (Code Division Multiple Access) receiver and CDMA transmitter with the device.

BACKGROUND ART

10 [0002] In a mobile communications environment, amplitude and phase fluctuations in a traffic channel can occur because of Rayleigh fading due to changes in the relative location between a mobile station and a base station. Thus, in a conventional phase modulation scheme that transmits data (information) by the phase of a carrier, it is common for a transmitting side to carry out differential encoding of transmitted data for impressing the data on relative phases of neighboring symbols, and for a receiving side to discriminate and decide the data by differential detection.

15 [0003] However, since the transmitted data is subjected to the differential encoding as mentioned above, a one-bit error in a radio section appears as a two-bit error in the differential detection, thereby increasing the receiving error rate by 3 dB in terms of the SNIR (Signal-to-Noise Interference power Ratio) as compared with coherent detection like binary phase-shift keyed modulation (BPSK modulation).

20 [0004] On the other hand, although absolute coherent detection, which discriminates and decides the phase of a received signal using the absolute phase of each data symbol, has a highly efficient receiving characteristic, it is difficult under the Rayleigh fading environment to decide the absolute phase of the reception.

25 [0005] In regard to this matter, Sadayuki Abeta, et al., "DS/CDMA Coherent Detection System with a Suppressed Pilot Channel", IEEE GLOBECOM'94, pp. 1622-1626, 1994, proposes a method of estimating fading distortion by inserting, in parallel with a data channel for transmitting data, a pilot channel which is orthogonal to the data channel and has known phases, thereby compensating for the fading distortion.

[0006] Fig. 13 illustrates a channel estimation method disclosed in this paper. In Fig. 13, the channel estimation is carried out using a pilot symbol sequence parallel with a data symbol sequence. To reduce a power loss, the power of the pilot symbol sequence is set less than that of the data symbol sequence.

30 [0007] In addition, to follow instantaneous Rayleigh fluctuations, the transmission power control is carried out on a slot by slot basis. Accordingly, as shown in Fig. 13, the amplitudes (powers) of the data symbol sequence and pilot symbol sequence vary slot by slot, and their phases also vary slightly due to the operation of amplifiers during transmission. Such transmission power control enables a reverse channel of the DS-CDMA (Direct Sequence CDMA) to maintain the SNIR against interference signals due to cross-correlation from other users.

35 [0008] The channel estimation of data symbols obtains its channel estimates by averaging (coherently adding) pilot symbols (estimated complex fading envelope) in a section (slot, in this case) to which the data symbols belong. The channel estimation with high SNIR is carried out in this manner. The estimates are employed to detect with the pilot symbols in the data symbol sections the received signal of a path of each user, to measure the amplitude and phase of the signal of each path, and to estimate and compensate for the channel fluctuations in the data symbol sections.

40 [0009] However, it is difficult for the foregoing method disclosed in the paper to achieve highly accurate channel estimation. This is because the method obtains the channel estimates by only averaging the pilot symbols in the slot including the data symbols to be subjected to the channel estimation.

45 [0010] Furthermore, in an actual mobile transmission environment, thermal noise (reducing the transmission power as low as possible creates a noise-limited environment, particularly at cell borders) and interference signals from other users due to cross-correlation are added to a desired signal of the channel to be received, and the phase and amplitude of the received signal vary every moment because of fading, which degrades the channel estimation accuracy. In summary, it is difficult for the method disclosed in the foregoing paper, which carries out the channel estimation of the data symbols using only the pilot symbols in the slot containing the data symbols, to achieve highly accurate channel estimation.

DISCLOSURE OF THE INVENTION

55 [0011] The present invention is implemented to solve the foregoing problems. It is therefore an object of the present invention to achieve highly accurate channel estimation by obtaining highly accurate channel estimates by calculating a sum of appropriately weighted pilot symbols when carrying out the channel estimation of the data symbols.

[0012] Furthermore, the present invention can achieve higher accuracy channel estimation by carrying out the channel estimation of the data symbols using pilot symbols not only in the slot containing the data symbols, but also in other slots.

[0013] The highly accurate channel estimation and compensation for channel fluctuations in the data symbols based on the channel estimation make it possible for the absolute coherent detection to decide the absolute phase of each data symbol even in the Rayleigh fading environment, which can reduce the SNIR for achieving desired receiving quality (receiving error rate). This can reduce the transmission power, and increase the capacity of a system in terms of the number of simultaneous subscribers.

[0014] In order to accomplish the object aforementioned, according to the invention as claimed in claim 1, a channel estimation unit for obtaining channel estimates of data symbols from a pilot symbol sequence which is parallel to a data symbol sequence comprises:

means for generating a plurality of pilot blocks from the pilot symbol sequence; and
means for obtaining the channel estimates of the data symbols by calculating a weighted sum of averages of the pilot symbols in the individual pilot blocks.

[0015] According to the invention as claimed in claim 2, a CDMA receiver which receives a data symbol sequence that is spread, and a pilot symbol sequence that is spread and parallel to the data symbol sequence, and which generates a data sequence by demodulating the spread data symbol sequence by using the spread pilot symbol sequence comprises:

means for receiving the spread data symbol sequence and the spread pilot symbol sequence;
means for generating a data symbol sequence by despread the spread data symbol sequence;
means for generating a pilot symbol sequence by despread the spread pilot symbol sequence;
means for generating from the pilot symbol sequence a plurality of pilot blocks;
means for obtaining channel estimates of the data symbols by calculating a weighted sum of averages of the pilot symbols in the individual pilot blocks;
means for compensating for channel fluctuations in the data symbol sequence by using the channel estimates of the data symbols; and
means for generating the data sequence by demodulating the data symbol sequence compensated for.

[0016] According to the invention as claimed in claim 3, in the CDMA receiver as claimed in claim 2, the spread data symbol sequence has been spread using a first spreading code, the spread pilot symbol sequence has been spread using a second spreading code, the means for generating the data symbol sequence despreads the spread data symbol sequence which has been spread using the first spreading code, and the means for generating the pilot symbol sequence despreads the spread pilot symbol sequence which has been spread using the second spreading code, and wherein the first spreading code and the second spreading code are orthogonal to each other.

[0017] According to the invention as claimed in claim 4, a CDMA transceiver have a transmitting processor and a receiving processor, the transmitting processor generate a spread data symbol sequence by modulating a data sequence, and transmits the spread data symbol sequence with a spread pilot symbol sequence which is spread in parallel with the data symbol sequence, and the receiving processor receives the spread data symbol sequence and the spread pilot symbol sequence, and generate the data sequence by demodulating the spread data symbol sequence by using the spread pilot symbol sequence, wherein
the transmitting processor comprises:

means for generating the data symbol sequence by modulating the data sequence;
means for generating the spread data symbol sequence by spreading the data symbol sequence;
means for generating the spread pilot symbol sequence by spreading the pilot symbol sequence; and
means for transmitting the spread data symbol sequence and the spread pilot symbol sequence, and wherein the receiving processor comprises:
means for receiving the spread data symbol sequence and the spread pilot symbol sequence;
means for generating the data symbol sequence by despread the spread data symbol sequence;
means for generating the pilot symbol sequence by despread the spread pilot symbol sequence;
means for generating from the pilot symbol sequence a plurality of pilot blocks;
means for obtaining channel estimates of the data symbols by calculating a weighted sum of averages of the pilot symbols in the individual pilot blocks;
means for compensating for channel fluctuations in the data symbol sequence by using the channel estimates of the data symbols; and
means for generating the data sequence by demodulating the data symbol sequence compensated for.

[0018] According to the invention as claimed in claim 5, in the CDMA transceiver as claimed in claim 4, the means for

generating the spread data symbol sequence spreads the data symbol sequence using a first spreading code, the means for generating the spread pilot symbol sequence spreads the pilot symbol sequence using a second spreading code, the means for generating the data symbol sequence despreads the spread data symbol sequence which has been spread using the first spreading code, and the means for generating the pilot symbol sequence despreads the spread pilot symbol sequence which has been spread using the second spreading code, and wherein the first spreading code and the second spreading code are orthogonal to each other.

[0019] According to the invention as claimed in claim 6, in the CDMA transceiver as claimed in claim 4 or 5, the means for transmitting the spread data symbol sequence and the spread pilot symbol sequence also transmits a spread power control symbol sequence for controlling power of the data symbol sequence and that of the pilot symbol sequence.

[0020] According to the invention as claimed in claim 7, in the CDMA transceiver as claimed in claim 6, the transmitting processor further comprises means for inserting into the data symbol sequence the power control symbol sequence.

[0021] According to the invention as claimed in claim 8, in the CDMA transceiver as claimed in claim 6, the transmitting processor further comprises means for inserting into the pilot symbol sequence the power control symbol sequence.

[0022] According to the invention as claimed in claim 9, in the CDMA transceiver as claimed in claim 6, the transmitting processor further comprises means for generating the spread power control symbol sequence by spreading the power control symbol sequence, and the means for transmitting the spread data symbol sequence and the spread pilot symbol sequence also transmits the power control symbol sequence.

[0023] According to the invention as claimed in claim 10, in the CDMA transceiver as claimed in claim 9, the means for generating a spread data symbol sequence spreads the data symbol sequence by using a first spreading code, the means for generating a spread pilot symbol sequence spreads the pilot symbol sequence by using a second spreading code, and the means for generating a spread power control symbol sequence spreads the power control symbol sequence by using a third spreading code, wherein the means for generating a data symbol sequence despreads the spread data symbol sequence by using the first spreading code, the means for generating a pilot symbol sequence despreads the spread pilot symbol sequence by using the second spreading code, and the means for generating a power control symbol sequence despreads the spread power control symbol sequence by using the third spreading code, and wherein the first spreading code, the second spreading code and the third spreading code are orthogonal to each other.

[0024] According to the invention as claimed in claim 11, in the CDMA transceiver as claimed in any one of claims 6-10, the receiving processor further comprises means for measuring from the pilot symbol sequence a signal-to-noise and interference power ratio, and for generating the power control symbol sequence from the signal-to-noise and interference power ratio.

[0025] According to the invention as claimed in claim 12, in the CDMA transceiver as claimed in any one of claims 4-11, the receiving processor further comprises means for generating the power control symbol sequence by despreads the spread power control symbol sequence for controlling the power of the data symbol sequence and that of the pilot symbol sequence; and means for extracting the power control symbol sequence, and wherein the means for receiving the spread data symbol sequence and the spread pilot symbol sequence receives the spread power control symbol sequence, and the means for transmitting the spread data symbol sequence and the spread pilot symbol sequence transmits the spread data symbol sequence and the spread pilot symbol sequence in accordance with the power control symbol sequence.

[0026] According to the invention as claimed in claim 13, in the equipment as claimed in any one of claims 1-12, the power of the data symbol sequence and that of the pilot symbol sequence are controlled on a slot by slot basis, and wherein the plurality of pilot blocks each consist of pilot symbols belonging to at least two different slots.

[0027] According to the invention as claimed in claim 14, in the equipment as claimed in any one of claims 1-13, when obtaining the channel estimate of an n th data symbol in the data symbol sequence, where n is an integer, the plurality of the pilot blocks each consist of pilot symbols from $(n-K+1)$ th pilot symbol to $(n+K)$ th pilot symbol in the pilot symbol sequence, where K is a natural number.

[0028] According to the invention as claimed in claim 15, in the equipment as claimed in any one of claims 1-14, the plurality of pilot blocks have a same length, each.

[0029] According to the invention as claimed in claim 16, in the equipment as claimed in any one of claims 1-15, when obtaining the channel estimate of an n th data symbol in the data symbol sequence, where n is an integer, the pilot blocks consisting of pilot symbols closer to the n th pilot symbol have a greater weight.

[0030] According to the invention as claimed in claim 17, a channel estimation method for obtaining channel estimates of data symbols from a pilot symbol sequence which is parallel with a data symbol sequence, comprises the steps of:

generating a plurality of pilot blocks from the pilot symbol sequence; and
obtaining the channel estimates of the data symbols by calculating a weighted sum of averages of the pilot symbols

in the individual pilot blocks.

[0031] According to the invention as claimed in claim 18, a CDMA receiving method which receives a data symbol sequence that is spread, and a pilot symbol sequence that is spread and parallel to the data symbol sequence, and which generates a data sequence by demodulating the spread data symbol sequence by using the spread pilot symbol sequence comprises the steps of:

receiving the spread data symbol sequence and the spread pilot symbol sequence;
generating a data symbol sequence by despread the spread data symbol sequence;
generating a pilot symbol sequence by despread the spread pilot symbol sequence;
generating from the pilot symbol sequence a plurality of pilot blocks;
obtaining channel estimates of the data symbols by calculating a weighted sum of averages of the pilot symbols in the individual pilot blocks;
compensating for channel fluctuations in the data symbol sequence by using the channel estimates of the data symbols; and
generating the data sequence by demodulating the data symbol sequence compensated for.

[0032] According to the invention as claimed in claim 19, a CDMA transmitting and receiving method which generates a spread data symbol sequence by modulating a data sequence, transmits the spread data symbol sequence with a pilot symbol sequence which is spread in parallel with the data symbol sequence, receives the spread data symbol sequence and the spread pilot symbol sequence, and generates the data sequence by demodulating the spread data symbol sequence by using the spread pilot symbol sequence, wherein

a transmitting side comprises the steps of:

generating the data symbol sequence by modulating the data sequence;
generating the spread data symbol sequence by spreading the data symbol sequence;
generating the spread pilot symbol sequence by spreading the pilot symbol sequence; and
transmitting the spread data symbol sequence and the spread pilot symbol sequence, and wherein
a receiving side comprises the steps of:
receiving the spread data symbol sequence and the spread pilot symbol sequence;
generating the data symbol sequence by despread the spread data symbol sequence;
generating the pilot symbol sequence by despread the spread pilot symbol sequence;
generating from the pilot symbol sequence a plurality of pilot blocks;
obtaining channel estimates of the data symbols by calculating a weighted sum of averages of the pilot symbols contained in the pilot blocks;
compensating for channel fluctuations in the data symbol sequence by using the channel estimates of the data symbols; and
generating the data sequence by demodulating the data symbol sequence compensated for.

BRIEF DESCRIPTION OF THE DRAWINGS

[0033]

Fig. 1 is a block diagram showing a configuration of a channel estimation unit as a first embodiment in accordance with the present invention;

Fig. 2 is a flowchart illustrating a channel estimation processing by the channel estimation unit of the first embodiment in accordance with the present invention;

Fig. 3 is a diagram illustrating, taking an example of the channel estimation, the principle of operation of the channel estimation by the channel estimation unit of the first embodiment in accordance with the present invention;

Fig. 4 is a block diagram showing a configuration of a CDMA receiver as a second embodiment in accordance with the present invention;

Fig. 5 is a flowchart illustrating a receiving processing by the CDMA receiver of the second embodiment in accordance with the present invention;

Fig. 6 is a block diagram showing a configuration of a CDMA transceiver as a third embodiment in accordance with the present invention;

Fig. 7 is a block diagram showing a configuration of a transmitting processor of the CDMA transceiver of the third embodiment in accordance with the present invention;

Fig. 8 is a block diagram showing a configuration of a receiving processor of the CDMA transceiver of the third

embodiment in accordance with the present invention;

Fig. 9 is a flowchart illustrating a transmitting processing by the transmitting processor of the CDMA transceiver of the third embodiment in accordance with the present invention;

Fig. 10 is a diagram illustrating a transmission example which inserts power control symbols into a data symbol sequence;

Fig. 11 is a diagram illustrating a transmission example which inserts power control symbols into a pilot symbol sequence;

Fig. 12 is a diagram illustrating a transmission example which transmits power control symbols in a sequence apart from the data symbol sequence and pilot symbol sequence; and

Fig. 13 is a diagram illustrating the principle of the channel estimation operation by a related art.

BEST MODE FOR CARRYING OUT THE INVENTION

[0034] Best modes for implementing the present invention will now be described in detail with reference to the accompanying drawings.

[FIRST EMBODIMENT]

[0035] Fig. 1 is a block diagram showing a configuration of a channel estimation unit as a first embodiment in accordance with the present invention. A channel estimation unit 100 of the present embodiment obtains channel estimates of data symbols from a pilot symbol sequence parallel with a data symbol sequence.

[0036] The channel estimation unit 100 comprises a pilot block generating section 110 and a channel estimate acquisition section 120. Although the channel estimation unit 100 is implemented in the present embodiment in the form of software using a DSP (Digital Signal Processor) (together with a memory that stores programs), it can be implemented in the form of hardware, in which case, components such as delay circuits are used as needed.

[0037] Fig. 2 is a flowchart illustrating a channel estimation processing by the channel estimation unit of the present embodiment, and Fig. 3 is a diagram illustrating, taking an example that obtains the channel estimates of an n th data symbol (n is a natural number), the operation principle of the channel estimation unit of the present embodiment. In the example of Fig. 3, both the data symbol sequence and the pilot symbol sequence undergo the transmission power control on a slot by slot basis.

[0038] First, at step S201, the pilot block generating section 110 generates from the pilot symbol sequence a plurality of pilot blocks. To generate L (three, in this example) pilot blocks each with a length of λ bits before and after the n th pilot symbol, the example as shown in Fig. 3 uses pilot symbols from $(n-K+1)$ th to $(n+K)$ th pilot symbols, where $K = L \times \lambda$ is a natural number.

[0039] It is preferable to generate the pilot blocks from pilot symbols belonging to different multiple slots to use these pilot symbols for the channel estimation. Using pilot symbols of different slots has an advantage of reducing the effect of the thermal noise and interference signals, which is greater than channel estimation error due to differences in power between the pilot symbols of different slots, making it possible to achieve the channel estimation at higher accuracy. In the example as shown in Fig. 3, six pilot blocks are generated from pilot symbols belonging to seven slots.

[0040] To obtain the channel estimate of the n th data symbol, it is not necessary to generate the same number of pilot blocks before and after the n th pilot symbol as in the example of Fig. 3. Thus, considering the delay of the channel estimation, the pilot blocks can be generated only from the pilot symbols with the number smaller than (previous to) the n th pilot symbol.

[0041] The length of a pilot block can be determined independently of the length of a slot. For example, the length of a pilot block can be equal to that of a pilot symbol, that is, a pilot block can consist of a single pilot symbol. Besides, the length of a pilot block can be varied from block to block.

[0042] At steps S202-S204, the channel estimate acquisition section 120 obtains the channel estimates of the data symbols. First, at step S202, the channel estimate acquisition section 120 calculates an average of the pilot symbols ξ (estimated complex fading envelope) in each pilot block to obtain the pilot block average $\bar{\xi}$, which is carried out for all the pilot blocks (step S203). When each pilot block consists of only one pilot symbol, the pilot symbol ξ itself becomes the pilot symbol average $\bar{\xi}$. In the example of Fig. 3, the pilot block averages $\bar{\xi}(n_i)$ are each obtained for i th pilot blocks ($i = -L$ to L , $i \neq 0$).

[0043] At step S204, the channel estimate acquisition section 120 calculates the weighted sum of the pilot block averages $\bar{\xi}$ to obtain the channel estimates $\bar{\xi}$ of the data symbol. In the example of Fig. 3, the channel estimate $\bar{\xi}(n)$ of the n th data symbol is obtained by placing the weights of the i th blocks at $\alpha(n_i)$. The channel estimate $\bar{\xi}(n)$ is given by the following equation (1).

$$\bar{\xi}(n) = \sum_{i=-L, i \neq 0}^L \alpha(n_i) \cdot \bar{\xi}(n_i) \quad (1)$$

[0044] It is preferable to increase the weights $\alpha(n_i)$ of the pilot blocks that include pilot symbols closer (closer in time) to the n th pilot symbol. This is because such pilot blocks can be considered to represent the state of the propagation path during the transmission of the n th data symbol more correctly because the propagation path fluctuates at every moment.

[0045] The channel estimate acquisition section 120 iterates the foregoing steps S201 - S204 for all the data symbols with which the channel estimates must be obtained (step S205).

[0046] Thus, highly accurate channel estimates can be obtained.

[SECOND EMBODIMENT]

[0047] Fig. 4 is a block diagram showing a configuration of a CDMA receiver as a second embodiment in accordance with the present invention. A CDMA receiver 400 of the present embodiment receives a data symbol sequence which is spread, and a pilot symbol sequence which is spread and parallel to the data symbol sequence, and restores the data sequence by demodulating the spread data symbol sequence using the spread pilot symbol sequence.

[0048] The CDMA receiver 400 comprises a receiving section 410, a data symbol sequence matched filter 424, a pilot symbol sequence matched filter 426, a channel estimation processor 428, a data symbol sequence compensator 430, a RAKE combiner 432, a deinterleaver 434 and a Viterbi decoder 436. Although these components such as the data symbol sequence matched filter 424, pilot symbol sequence matched filter 426 and so forth are implemented in the form of software using a DSP (and a memory that stores programs) 420 as shown in Fig. 4 in the present embodiment, they can be implemented with hardware. The structure and functions of the channel estimation processor 428 are the same as those of the channel estimation unit 100 of the first embodiment in accordance with the present invention.

[0049] Fig. 5 is a flowchart illustrating a receiving processing by the CDMA receiver of the second embodiment in accordance with the present invention. First, at step S501, the receiving section 410 receives the received signal, that is, the spread data symbol sequence and the spread pilot symbol sequence.

[0050] In the present embodiment, it is assumed that the received data symbol sequence and pilot symbol sequence have been spread using a first spreading code and a second spreading code, respectively, which are orthogonal to each other. At step S502, the data symbol sequence matched filter 424 despreads the received signal using the first spreading code, thereby generating the data symbol sequence. At step S503, the pilot symbol sequence matched filter 426 despreads the received signal using the second spreading code, thereby generating the pilot symbol sequence.

[0051] At step S504, the channel estimation processor 428 carries out a channel estimation processing to obtain the channel estimates of the data symbols. The channel estimation processing is the same as that of the channel estimation unit 100 (Fig. 2) of the first embodiment in accordance with the present invention.

[0052] At step S505, the data symbol sequence compensator 430 compensates for the channel fluctuations in the data symbol sequence using the channel estimates $\bar{\xi}$. More specifically, it compensates for the channel fluctuations in the data symbols by multiplying the data symbol sequence by the complex conjugates of the channel estimates $\bar{\xi}$.

[0053] At step S506, the RAKE combiner 432, deinterleaver 434 and Viterbi decoder 436 generates the data sequence by demodulating the compensated data symbol sequence. The RAKE combiner 432 carries out the in-phase combining of the compensated data symbol sequence fed from individual RAKE fingers.

[0054] Thus, the receiving processing can achieve highly accurate channel estimation, and the compensation for the channel fluctuations in the data symbol sequence.

[THIRD EMBODIMENT]

[0055] Fig. 6 is a block diagram showing a configuration of a CDMA transceiver as a third embodiment in accordance with the present invention. A CDMA transceiver 600 of the present embodiment comprises a transmitting processor 610 and a receiving processor 620. The transmitting processor 610 generates a spread data symbol sequence by modulating a data sequence, and transmits the spread data symbol sequence along with a pilot symbol sequence which is parallel with the data symbol sequence and undergoes spreading. The receiving processor 620 receives the spread data symbol sequence and the spread pilot symbol sequence, and demodulates the spread data symbol sequence using the spread pilot symbol sequence to generate the data sequence. In the present embodiment, this station (the present CDMA transceiver) exchanges power control symbols with a party station. The power control symbols are symbols

(command) for controlling power of the data symbol sequence and the pilot symbol sequence.

[0056] Fig. 7 shows a configuration of the transmitting processor 610, and Fig. 8 shows a configuration of the receiving processor.

[0057] As shown in Fig. 7, the transmitting processor 610 comprises a transmitting section 710, a channel encoder 722, an inserting section 724, a data symbol sequence spreader 726, a pilot symbol sequence spreader 728 and a combiner 730. Although these components such as the channel encoder 722, inserting section 724 and so forth are implemented in the form of software using a DSP (and a memory that stores programs) 720 in the present embodiment, they can be implemented with hardware.

[0058] Fig. 9 is a flowchart illustrating a transmitting processing by the transmitting processor of the CDMA transceiver of the present embodiment. First, at step S901, the channel encoder 722 generates the data symbol sequence by modulating (encoding) the data sequence.

[0059] At step S902, the inserting section 724 inserts into the data symbol sequence the power control symbol sequence the party station uses to determine the power of a data symbol sequence and a pilot symbol sequence to be transmitted from the party station to the present station.

[0060] Fig. 10 is a diagram illustrating an example that inserts the power control symbols into the data symbol sequence to be transmitted, in which case, the power control symbols are inserted into the data symbol sequence at every one-slot interval.

[0061] Although the power control symbols are inserted into the data symbol sequence to be transmitted in the present embodiment, they can be inserted into the pilot symbol sequence to be transmitted, or they can be transmitted as another sequence in addition to the data symbol sequence and pilot symbol sequence, as will be described later.

[0062] Returning to Fig. 9, at step S903, the data symbol sequence spreader 726 spreads the data symbol sequence using the first spreading code to generate the spread data symbol sequence. At step S904, the pilot symbol sequence spreader 728 spreads the pilot symbol sequence using the second spreading code to generate the spread pilot symbol sequence. The first spreading code and the second spreading code are orthogonal to each other.

[0063] At step S905, the combiner 730 combines the spread data symbol sequence and the spread pilot symbol sequence to generate a transmitted signal.

[0064] At step S906, the transmitting section 710 transmits the transmitted signal in accordance with a power control symbol sequence which is sent from the party station to the present station.

[0065] Next, as shown in Fig. 8, the receiving processor 620 comprises a receiving section 810, a data symbol sequence matched filter 824, a pilot symbol sequence matched filter 826, a channel estimation processor 828, a data symbol sequence compensator 830, a RAKE combiner 832, a deinterleaver 834, a Viterbi decoder 836, a power control symbol generator 838 and a power control symbol sequence extracting section 840. Although these components such as the data symbol sequence matched filter 824, pilot symbol sequence matched filter 826 and so forth are implemented in the form of software using a DSP (and a memory that stores programs) 820 in the present embodiment, they can be implemented with hardware. The structure and functions of the channel estimation processor 828 are the same as those of the channel estimation unit 100 of the first embodiment in accordance with the present invention, and the structure and functions of the receiving section 810, data symbol sequence matched filter 824 and so forth are the same as those of their counterparts of the CDMA receiver of the second embodiment. Accordingly, the receiving processor 620 carries out the same processings as those (Fig. 5) of the CDMA receiver of the second embodiment in accordance with the present invention.

[0066] The power control symbol generator 838 measures the SNIR from the pilot symbol sequence supplied from the pilot symbol sequence matched filter 826, and generates the power control symbols in response to the measured values. As a measuring method of the SNIR, there is a method of measuring it by obtaining the average and variance of the received signal. The SNIR measurement can also use feedback data symbol sequence after decision. The power control symbols generated here are supplied to the inserting section 724 of the transmitting processor 610, which inserts them into the data symbol sequence when transmitting the next signal to the party station to be transmitted. Receiving the symbols, the party station uses them when transmitting a signal to the present station.

[0067] The power control symbol sequence extracting section 840 extracts from the data symbol sequence the power control symbol sequence, and supplies it to the transmitting section 710 of the transmitting processor 610 to be used when transmitting the next signal to the party station.

[0068] The power control symbols can also be inserted into the pilot symbol sequence. Fig. 11 is a diagram illustrating a transmission example in which power control symbols are inserted into a pilot symbol sequence to be transmitted. To insert the power control symbols into the pilot symbol sequence, a component corresponding to the inserting section 724 is installed in the transmitting processor 610 so that it inserts into the pilot symbol sequence the power control symbol sequence, and a component corresponding to the power control symbol sequence extracting section 840 is installed in the receiving processor 620 so that it extracts from the pilot symbol sequence the power control symbol sequence.

[0069] The power control symbols can be transmitted as another sequence in addition to the data symbol sequence and the pilot symbol sequence. Fig. 12 is a diagram illustrating a transmission example in which the power control sym-

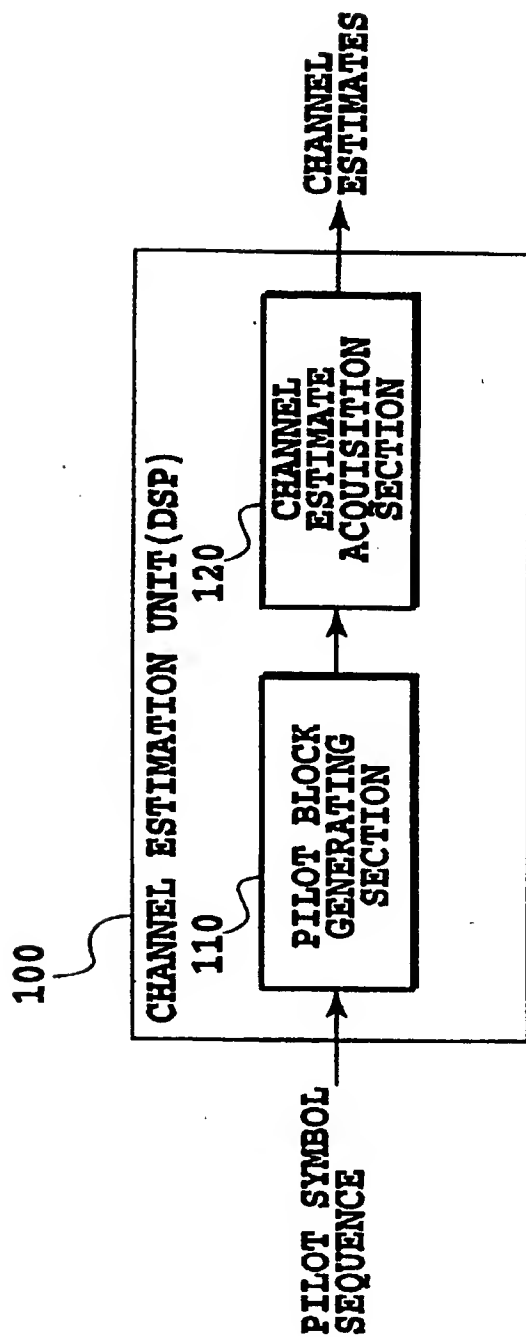
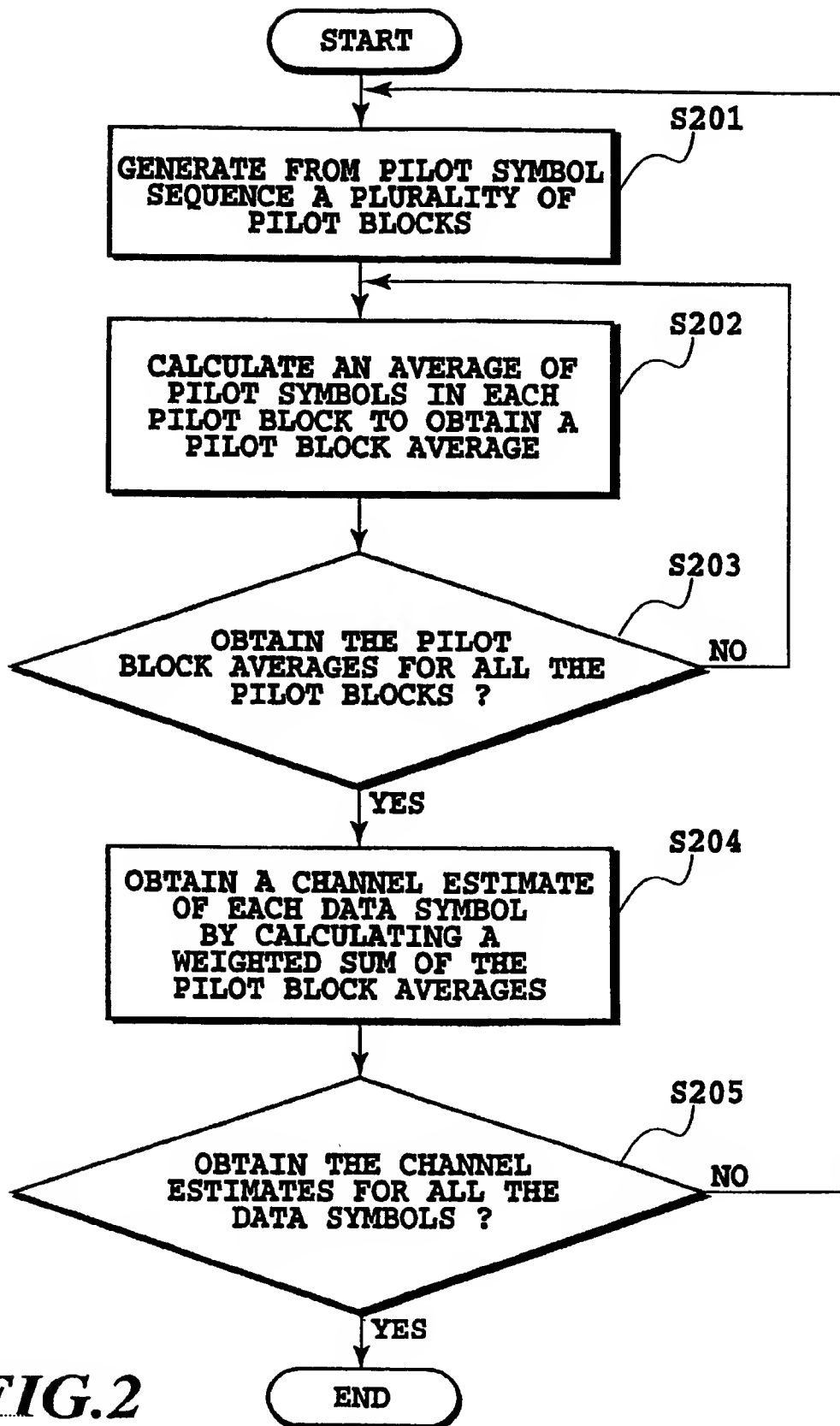


FIG.1



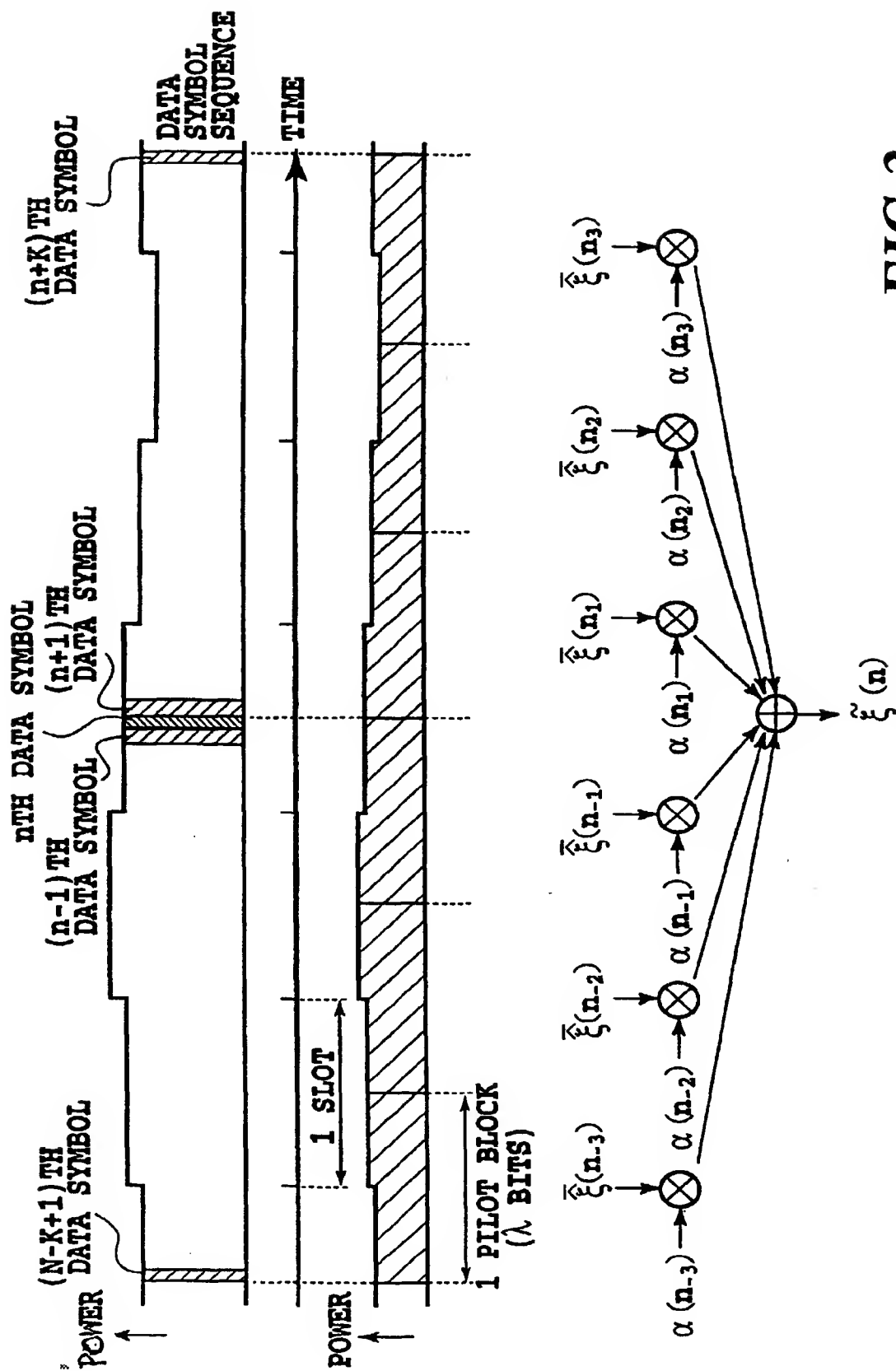


FIG.3

CHANNEL ESTIMATE OF n^{TH} DATA SYMBOL

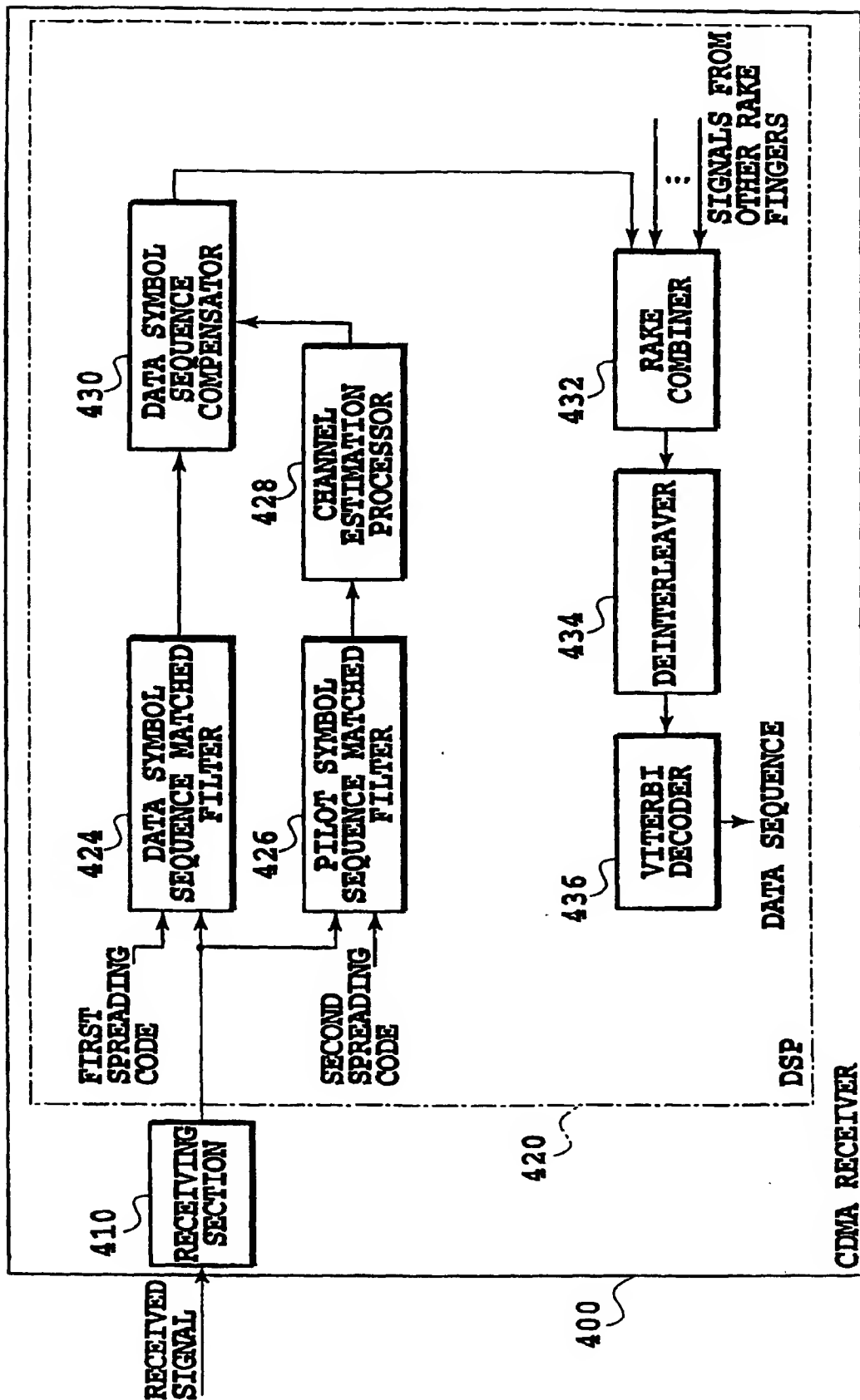
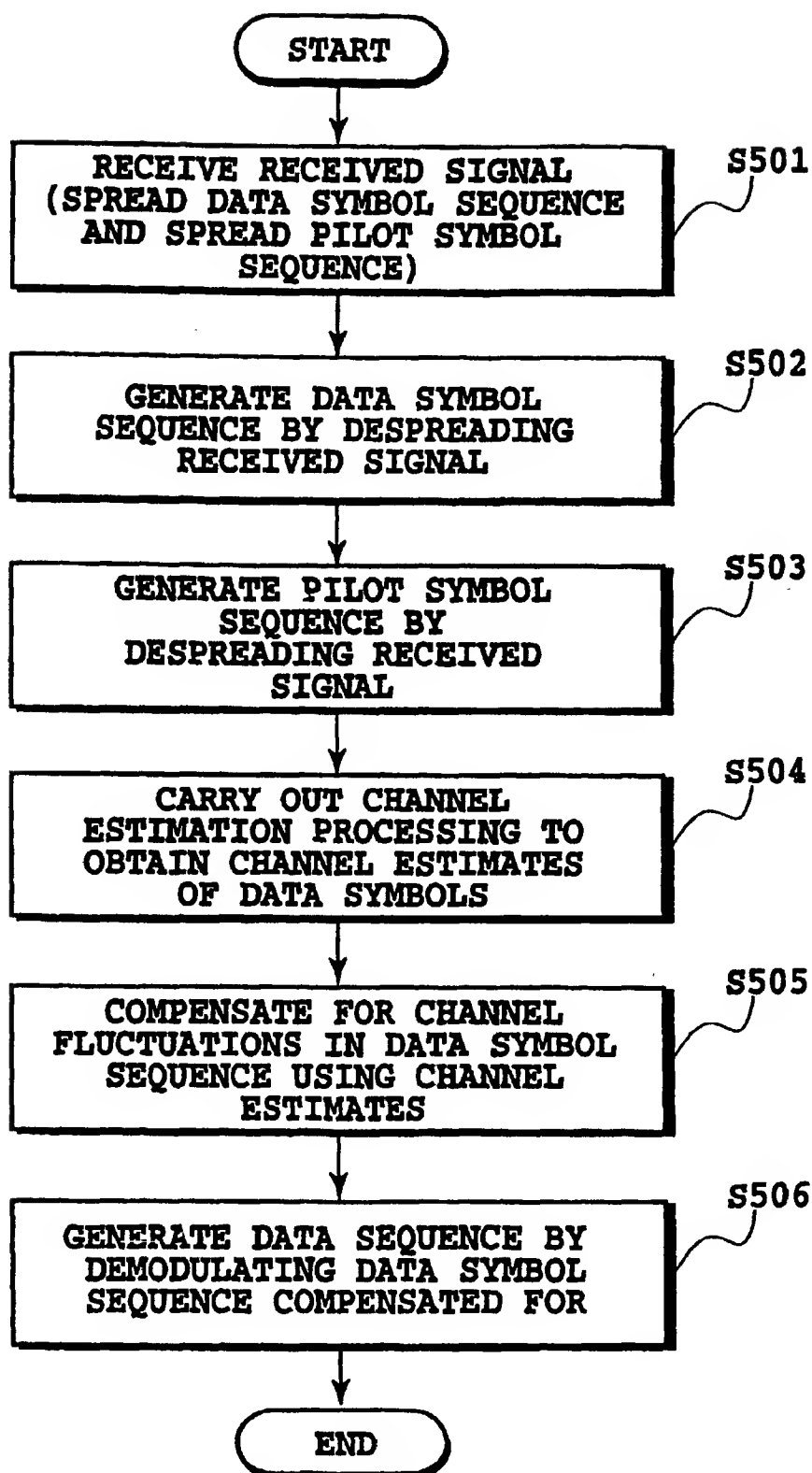


FIG. 4

**FIG.5**